



PATENT
Docket No. 306812002601

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Victoria Wilson
Victoria Wilson

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

David LEWIS et al.

Serial No.: 10/623,709

Filing Date: July 21, 2003

For: ROUTING ARCHITECTURE FOR A
PROGRAMMABLE LOGIC DEVICE

Examiner: To Be Assigned

Group Art Unit: To Be Assigned

**INFORMATION DISCLOSURE
STATEMENT UNDER 37 C.F.R. § 1.97 & 1.98**

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.97 and § 1.98, Applicants submit for consideration in the above-identified application the documents listed on the attached Form PTO-1449. Copies of the documents were previously submitted in an Information Disclosure Statement and/or Office Action, directed to the related application Serial Number 10/140,287, filed May 6, 2002, and, accordingly, copies are not included herewith. This protocol conforms with 37 C.F.R. §1.98(d) and M.P.E.P. 609(A)(2). The Examiner is requested to make these documents of record in the application.

This Information Disclosure Statement is submitted:

- ☐ With the application; accordingly, no fee or separate requirements are required.
- ☒ Within three months of the application filing date or before mailing of a first Office Action on the merits; accordingly, no fee or separate requirements are required.
- ☐ After receipt of a first Office Action on the merits but before mailing of a final Office Action or Notice of Allowance.
 - ☐ A fee is required. A check in the amount of * is enclosed.
 - ☐ A fee is required. Accordingly, a Fee Transmittal form (PTO/SB/17) is attached to this submission in duplicate.
 - ☐ A Certification under 37 C.F.R. § 1.97(e) is provided below; accordingly, no fee is believed to be due.
- ☐ After mailing of a final Office Action or Notice of Allowance, but before payment of the issue fee.
 - ☐ A Certification under 37 C.F.R. § 1.97(e) is provided below and a check in the amount of * is enclosed.
 - ☐ A Certification under 37 C.F.R. § 1.97(e) is provided below and a Fee Transmittal form (PTO/SB/17) is attached to this submission in duplicate.

Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

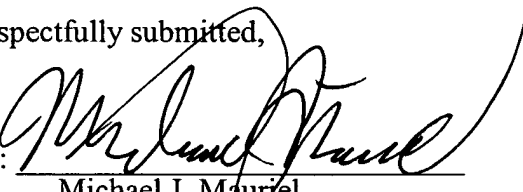
The information contained in this Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

In the unlikely event that the transmittal letter is separated from this document and the Patent Office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to **Deposit Account No. 03-1952** referencing 306812002601. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Dated: August 8, 2003

Respectfully submitted,

By:



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Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary)	Docket Number 306812002601	Application Number 10/623,709
	Applicant David M. LEWIS, et al.	
	Filing Date July 21, 2003	Group Art Unit To Be Assigned
	Mailing Date August 8, 2003	

U.S. PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Name	Class	Subclass	Filing Date If Appropriate
	1.	9/26/89	4,870,302	Freeman			
	2.	9/7/93	5,243,238	Kean			
	3.	11/9/93	5,260,611	Cliff, et al.			
	4.	10/3/95	5,455,525	Ho, et al.			
	5.	1/16/96	5,485,103	Pederson, et al.			
	6.	7/16/96	5,537,057	Leong, et al.			
	7.	7/30/96	5,541,530	Cliff, et al.			
	8.	12/3/96	5,581,199	Pierce, et al.			
	9.	1/7/97	5,592,106	Leong, et al.			
	10.	10/28/97	5,682,107	Tavana, et al.			
	11.	11/18/97	5,689,195	Cliff, et al.			
	12.	12/23/97	5,701,091	Kean			
	13.	1/6/98	5,705,939	McClintock, et al.			
	14.	12/8/98	5,847,579	Trimberger			
	15.	3/9/99	5,880,598	Duong			
	16.	5/25/99	5,907,248	Bauer, et al.			
	17.	6/1/99	5,909,126	Cliff, et al.			
	18.	6/22/99	5,914,616	Young, et al.			
	19.	8/24/99	5,942,913	Young, et al.			
	20.	7/4/00	6,084,429	Trimberger			
	21.	8/22/00	6,107,824	Reddy, et al.			
	22.	3/20/01	6,204,690 B1	Young, et al.			
	23.	8/21/01	6,278,291 B1	McClintock, et al.			
	24.	9/18/01	6,292,018 B1	Kean			
	25.	10/9/01	6,300,794 B1	Reddy, et al.			

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not the citation conforms with MPEP 609. Draw a line through the citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.



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FOREIGN PATENT DOCUMENTS

Examiner Initials	Ref. No.	Date	Document No.	Country	Class	Subclass	Translation YES NO	

OTHER DOCUMENTS

(including author, title, Date, Pertinent Pages, Etc.)

Examiner Initials	Ref. No.	Title
	26.	Kluwer Academic Publishers, Vaughn Betz, Jonathan Rose, Alexander Marquardt, <u>Architecture and CAD for Deep-Submicron FPGAs</u> , Chapter 2.1 (pp. 11-18), Chapter 4 (pp. 63-103), Chapter 5 (pp. 105-126), and Chapter 7 (pp. 151-190), (March 1999)

EXAMINER:	DATE CONSIDERED:
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